Analog Power AM7302N

# N-Channel 30-V (D-S) MOSFET

# **Key Features:**

- Low r<sub>DS(on)</sub> trench technology
- · Low thermal impedance
- · Fast switching speed

### **Typical Applications:**

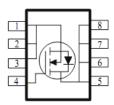
- · White LED boost converters
- Automotive Systems
- Industrial DC/DC Conversion Circuits

PRODUCT SUMMARY				
V <sub>DS</sub> (V)	$r_{DS(on)}(m\Omega)$	I <sub>D</sub> (A)		
30	4 @ V <sub>GS</sub> = 10V	24.2		
	6 @ V <sub>GS</sub> = 4.5V	19.8		





DFN3x3-8L



ABSOLUTE MAXIMUM RATINGS (T <sub>A</sub> = 25°C UNLESS OTHERWISE NOTED)						
Parameter		Symbol	Limit	Units		
Drain-Source Voltage			30	V		
Gate-Source Voltage		$V_{GS}$	±20	V		
Continuous Drain Current®	T <sub>A</sub> =25°C	ı	24.2	А		
Continuous Drain Current <sup>a</sup>	T <sub>A</sub> =70°C	i <sub>D</sub>	18.3			
Pulsed Drain Current <sup>b</sup>		I <sub>DM</sub>	100			
Continuous Source Current (Diode Conduction) a		I <sub>S</sub>	5	Α		
Device Discinction <sup>a</sup>	T <sub>A</sub> =25°C	$P_{D}$	3.5	W		
Power Dissipation <sup>a</sup>	T <sub>A</sub> =70°C	L.D	2	VV		
Operating Junction and Storage Temperature Range		$T_J$ , $T_{stq}$	-55 to 150	°C		

THERMAL RESISTANCE RATINGS							
Parameter			Maximum	Units			
Maximum Junction-to-Ambient <sup>a</sup>	t <= 10 sec	$R_{\theta JA}$	35	°C/W			
Maximum Junction-to-Ambient	Steady State	IXOJA	81	C/VV			

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### Notes

- a. Surface Mounted on 1" x 1" FR4 Board.
- b. Pulse width limited by maximum junction temperature

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### **Electrical Characteristics**

Parameter	Symbol	Test Conditions	Min	Тур	Max	Unit	
Static							
Gate-Source Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}$ , $I_D = 250 \text{ uA}$	1			V	
Gate-Body Leakage	I <sub>GSS</sub>	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			±100	nA	
Zoro Coto Voltogo Droin Correct	1	$V_{DS} = 24 \text{ V}, V_{GS} = 0 \text{ V}$			1	uA	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	$V_{DS} = 24 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 55^{\circ}\text{C}$	<sub>S</sub> = 0 V, T <sub>J</sub> = 55°C		25	uA	
On-State Drain Current	I <sub>D(on)</sub>	$V_{DS} = 5 \text{ V}, V_{GS} = 10 \text{ V}$	30			Α	
Drain-Source On-Resistance	r	$V_{GS} = 10 \text{ V}, I_{D} = 20 \text{ A}$			4	mΩ	
Dialii-Source Oil-Resistance	r <sub>DS(on)</sub>	$V_{GS} = 4.5 \text{ V}, I_D = 16 \text{ A}$			6	11122	
Forward Transconductance	g <sub>fs</sub>	$V_{DS} = 15 \text{ V}, I_{D} = 20 \text{ A}$		25		S	
Diode Forward Voltage	$V_{SD}$	$I_{S} = 2.5 \text{ A}, V_{GS} = 0 \text{ V}$		0.72		V	
		Dynamic					
Total Gate Charge	$Q_g$	$V_{DS} = 15 \text{ V}, V_{GS} = 4.5 \text{ V},$		32		nC	
Gate-Source Charge	$Q_{gs}$	$I_{DS} = 13 \text{ V}, \text{ V}_{GS} = 4.3 \text{ V},$ $I_{D} = 20 \text{ A}$		13			
Gate-Drain Charge	$Q_gd$	1D = 20 A		13			
Turn-On Delay Time	t <sub>d(on)</sub>	$V_{DS} = 15 \text{ V}, R_{L} = 0.8 \Omega,$		13			
Rise Time	t <sub>r</sub>	$V_{DS} = 13 \text{ V}, \text{ K}_{L} = 0.6 \Omega,$ $I_{D} = 20 \text{ A},$		15		ne	
Turn-Off Delay Time	$t_{d(off)}$	$V_{GEN} = 10 \text{ V}, R_{GEN} = 6 \Omega$		75		ns	
Fall Time	t <sub>f</sub>	VGEN = 10 V, NGEN = 0 12		25			
Input Capacitance	C <sub>iss</sub>			5022			
Output Capacitance	C <sub>oss</sub>	$V_{DS} = 15 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$		424		pF	
Reverse Transfer Capacitance	$C_{rss}$			358			

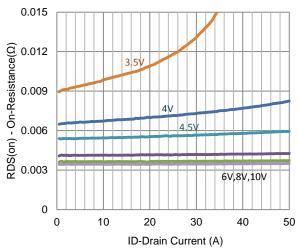
#### Notes

- Pulse test: PW <= 300us duty cycle <= 2%.
- Guaranteed by design, not subject to production testing. b.

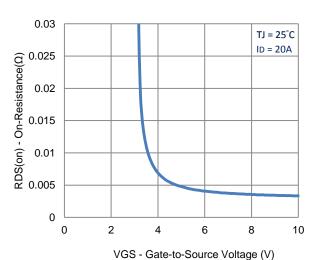
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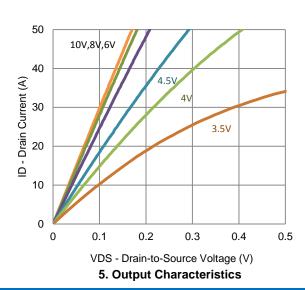
### **Typical Electrical Characteristics**



#### 1. On-Resistance vs. Drain Current



3. On-Resistance vs. Gate-to-Source Voltage



100

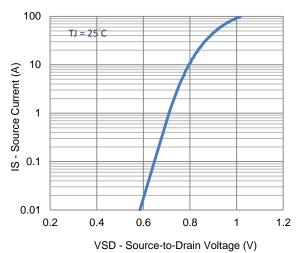
TJ = 25°C

80

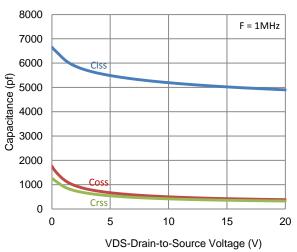
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2. Transfer Characteristics



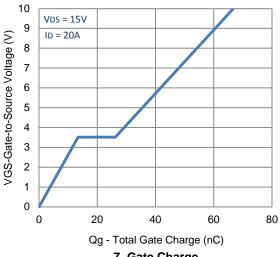
4. Drain-to-Source Forward Voltage

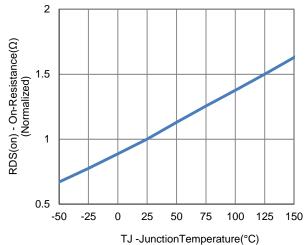


6. Capacitance

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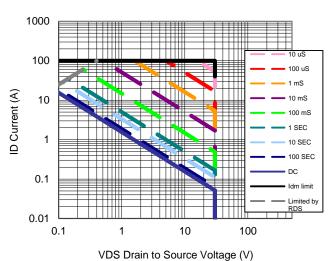
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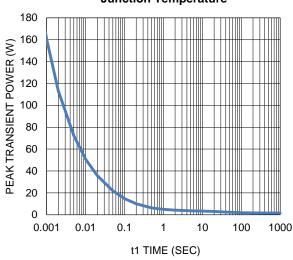




7. Gate Charge

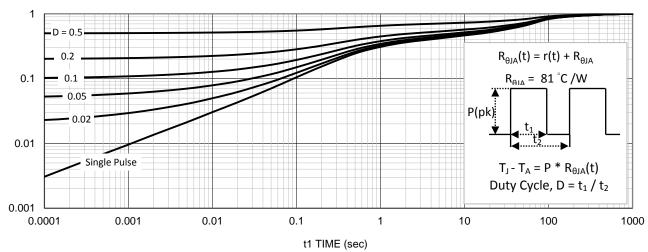






9. Safe Operating Area

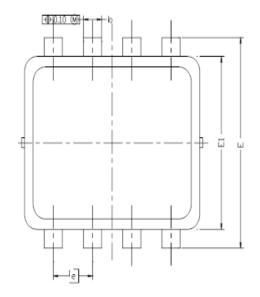
10. Single Pulse Maximum Power Dissipation

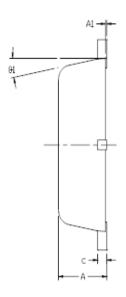


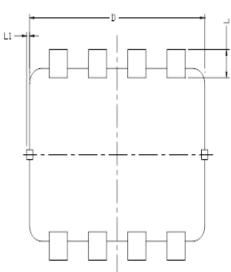
11. Normalized Thermal Transient Junction to Ambient

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# Package Information







DIM.	MILLIMETERS			INCHES			
	MIN	NDM	MAX	MIN	NDM	MAX	
Α	0.700	0.80	0,900	0.0276	0.0315	0.0354	
A1	0.00		0.05	0,000		0.002	
b	0.24	0.30	0.35	0.009	0.012	0.014	
_	0.08	0.152	0.25	0.003	0,006	0.010	
D	2.90 BSC			0.114 BSC			
E	2.80 BSC			0.110 BSC			
E1	2.30 BSC			0.091 BSC			
9	0.65 BSC		0.026 BSC				
L	0.20	0.375	0.450	0.008	0.0148	0.0177	
L1	0		0.100	0		0.004	
91	0	10	12	0	10	12	